

IRL Dresden Testbed

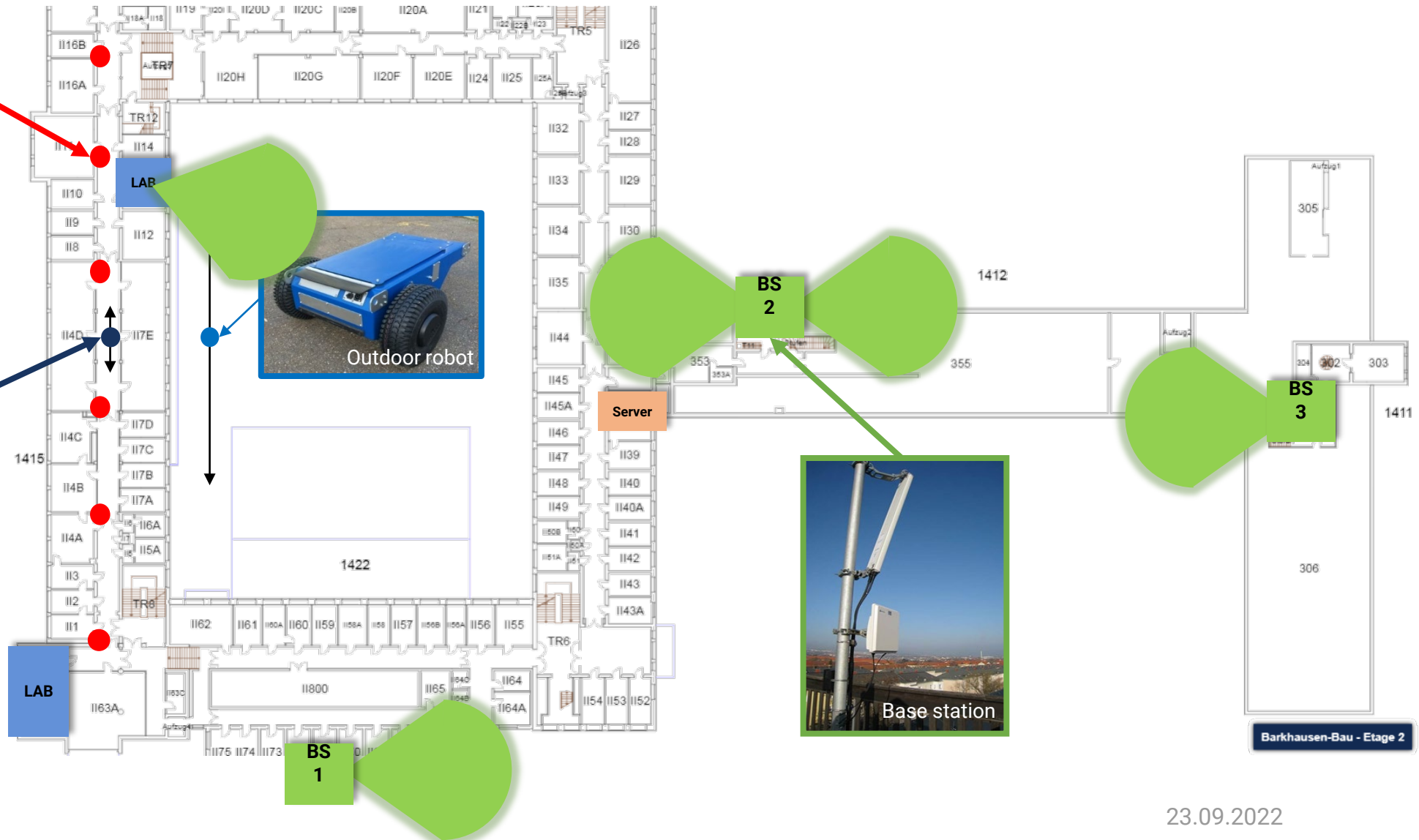
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GEFÖRDERT VOM



Bundesministerium
für Bildung
und Forschung

Testbed Layout

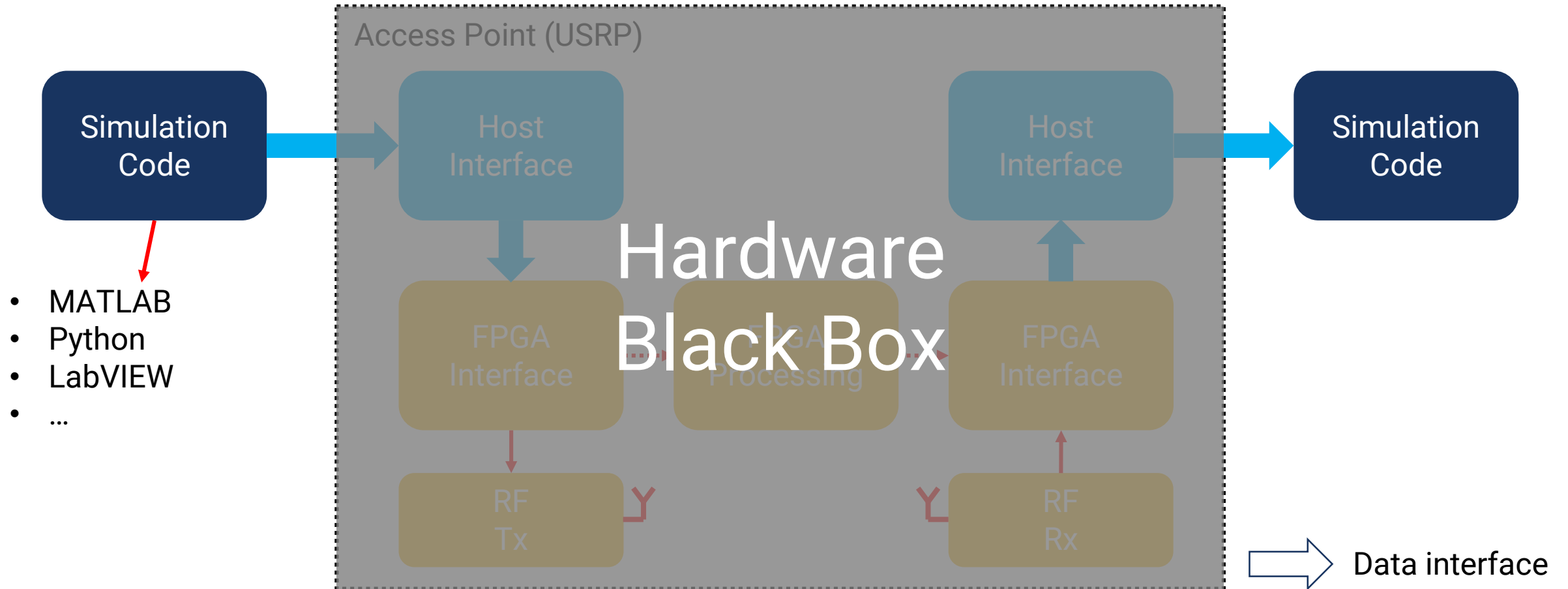


Experimental Node

- RF Capabilities
 - 4 x Ettus UBX Daughterboards
 - Frequency range: **10 MHz to 6 GHz**
 - Bandwidth: **160 MHz**
 - **26-28 GHz** possible
 - Antenna: **4x4 MIMO / Omnidirectional**
 - Maximum Output power: **26 dBm**
 - Spectrum sensing device
- Processing Capabilities
 - CPU: **Intel i7**, 16 GB RAM, 256 GB SSD
 - FPGA: 2 x Xilinx **Kintex 7 – 410T**
 - GPU: NVIDIA Jetson with **512 CUDA Cores**



Hardware-in-Loop



Framework: Automated Measurement

